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APPLICANTS

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** CONTINUING DATA *****

This application is a DIV of 09/979,188 11/20/2001 PAT 6,633,078
which is a 371 of PCT/JP01/02171 03/19/2001

** FOREIGN APPLICATIONS *****

JAPAN 2000-078443 03/21/2000

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 10/24/2003

Foreign Priority claimed	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	JAPAN	DRAWING 11	CLAIMS 4	CLAIMS 1
Verified and Acknowledged	<i>[Signature]</i> Examiner's Signature	<i>[Initials]</i>			

ADDRESS

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TITLE

Semiconductor device, method for manufacturing an electronic equipment, electronic equipment, and portable information terminal

<input type="checkbox"/> All Fees
<input type="checkbox"/> 1.16 Fees (Filing)
<input type="checkbox"/> 1.17 Fees (Processing Ext. of time)
FILING FEE FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT